

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	AUTOMATIC BIT FAIL MAPPING FOR EMBEDDED MEMORIES WITH CLOCK MULTIPLIERS																																																
<p>Application Number : 10/707071</p> <p>Confirmation Number:</p> <p>First Named Applicant: Darren Anand</p> <p>Attorney Docket Number: BUR920030168US1</p> <p>Art Unit: 2117</p> <p>Examiner: KERVEROS, JAMES</p> <p>Search string: (4876685 or 5936876 or 6185709 or 6509766 or 0084387).pn</p>																																																	
<p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>init</th> <th>Cite.No.</th> <th>Patent No.</th> <th>Date</th> <th>Patentee</th> <th>Kind</th> <th>Class</th> <th>Subclass</th> </tr> </thead> <tbody> <tr> <td>/JK/</td> <td>1</td> <td>4876685</td> <td>1989-10-24</td> <td>Rich</td> <td></td> <td></td> <td></td> </tr> <tr> <td></td> <td>2</td> <td>5936876</td> <td>1999-08-10</td> <td>Sugasawara</td> <td></td> <td></td> <td></td> </tr> <tr> <td></td> <td>3</td> <td>6185709</td> <td>2001-02-06</td> <td>Dreibelbis et al</td> <td></td> <td></td> <td></td> </tr> <tr> <td></td> <td>4</td> <td>6509766</td> <td>2003-01-21</td> <td>Pomichter et al</td> <td></td> <td></td> <td></td> </tr> <tr> <td></td> <td>5</td> <td>0084387</td> <td>2003-05-01</td> <td>Rooney et al</td> <td></td> <td></td> <td></td> </tr> </tbody> </table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	/JK/	1	4876685	1989-10-24	Rich					2	5936876	1999-08-10	Sugasawara					3	6185709	2001-02-06	Dreibelbis et al					4	6509766	2003-01-21	Pomichter et al					5	0084387	2003-05-01	Rooney et al			
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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /JK/